

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

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In re Patent Application of:  
James N. Dieffenderfer et al.

Confirmation No.: 9456

Application No.: 10/810,235

Filed: March 26, 2004

Art Unit: 2183

For: APPARATUS AND METHOD FOR  
DECREASING THE LATENCY BETWEEN  
INSTRUCTION CACHE AND A PIPELINE  
PROCESSOR

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Examiner: Aimee J. Li

**AMENDMENT IN RESPONSE TO NON-FINAL OFFICE ACTION**

**MS Amendment**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

**INTRODUCTORY COMMENTS**

In response to the Office Action dated October 5, 2006, please amend the above-identified U.S. patent application as follows:

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks/Arguments** begin on page 7 of this paper.

No fees are believed to be due with this amendment.